

Fig. 1

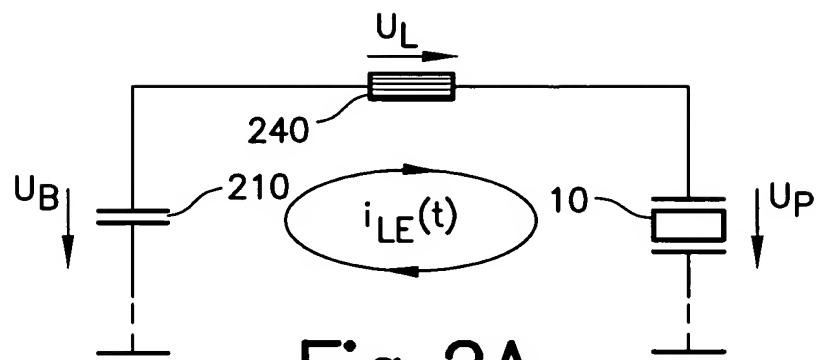


Fig. 2A

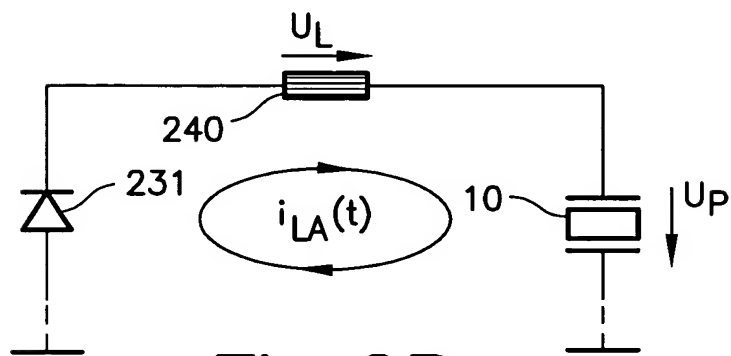


Fig. 2B

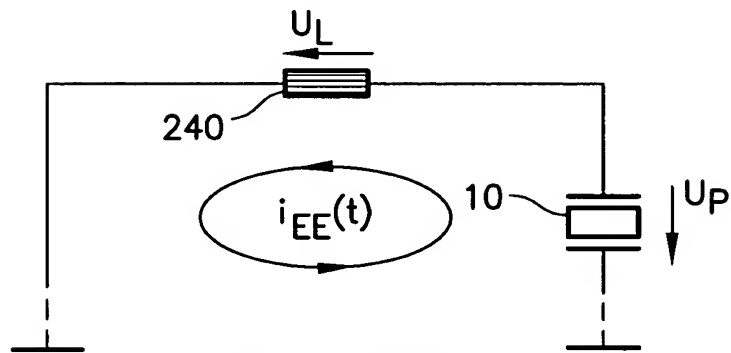


Fig. 2C

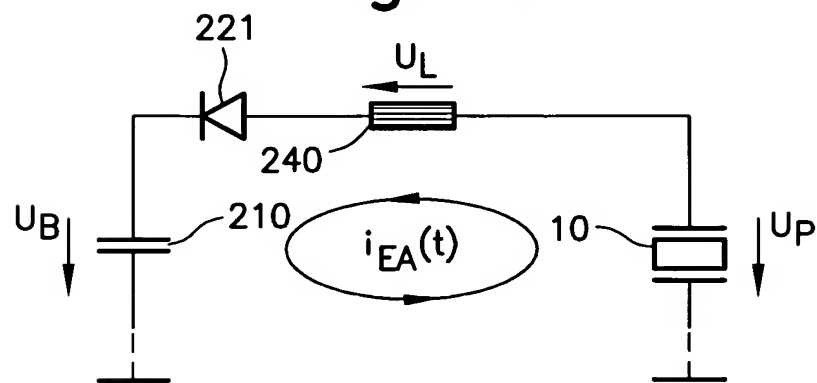


Fig. 2D

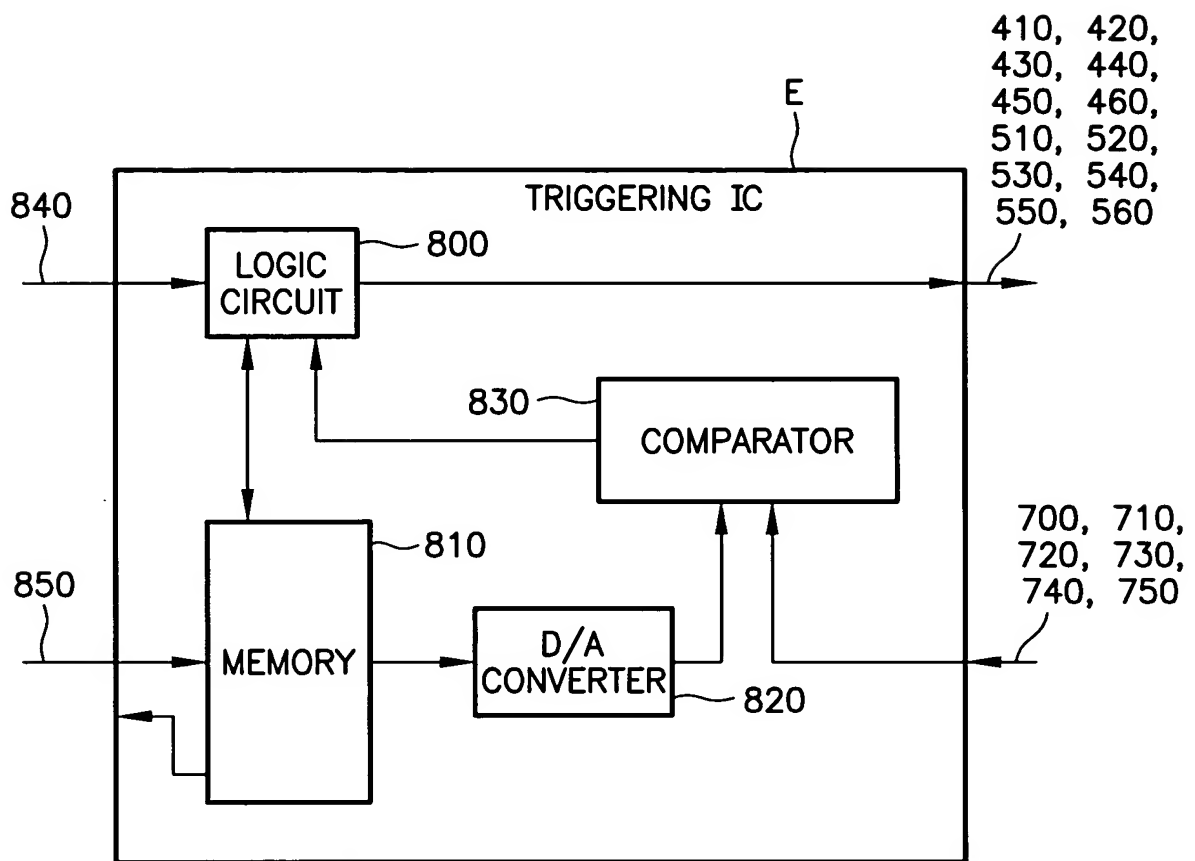


Fig. 3

Time sequence of interrupts (existing art)

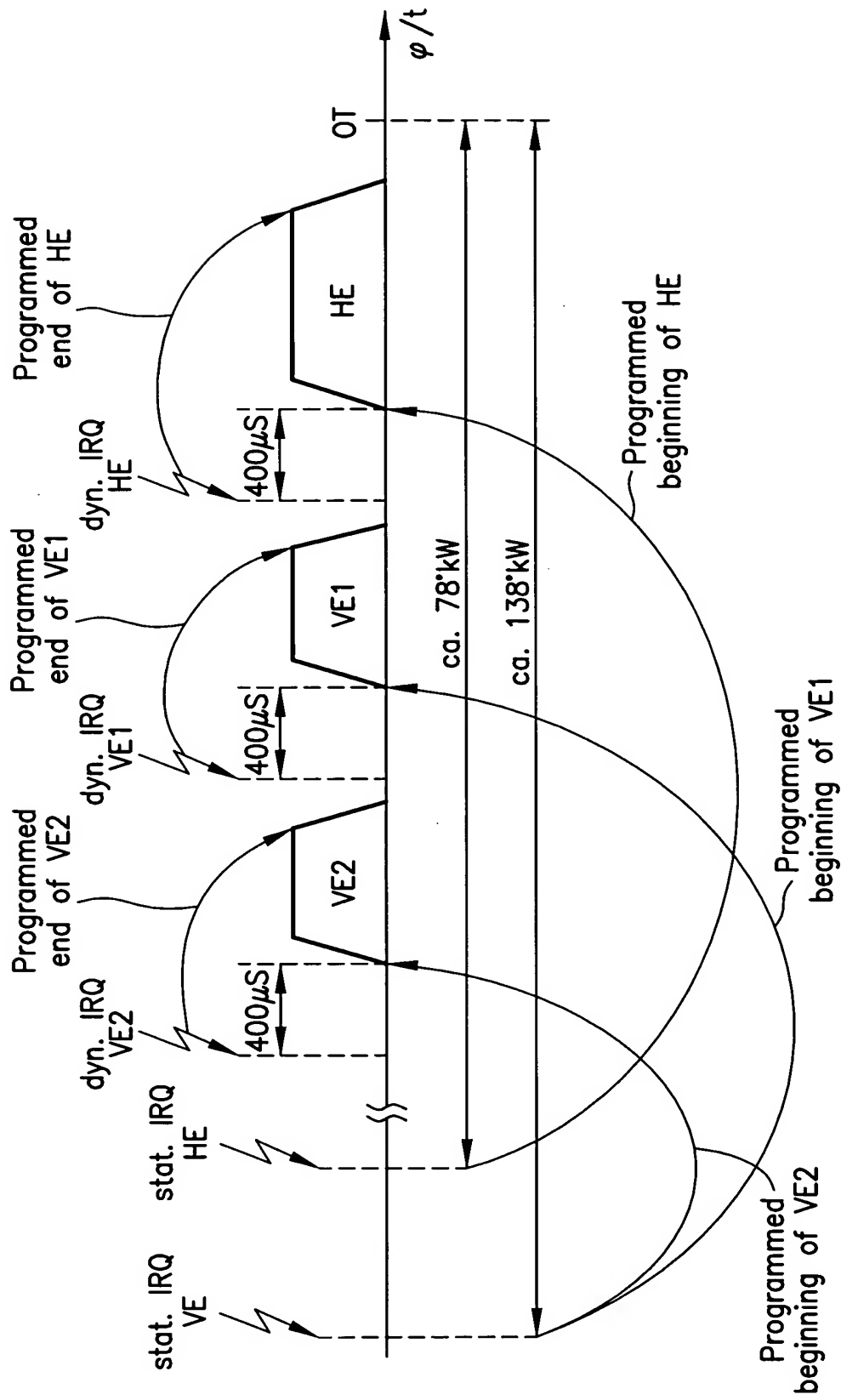


Fig. 4

$$t_K \leq \varphi_{\text{pre}}$$

Fig. 5

Shifting a low-priority edge later (Calculation)

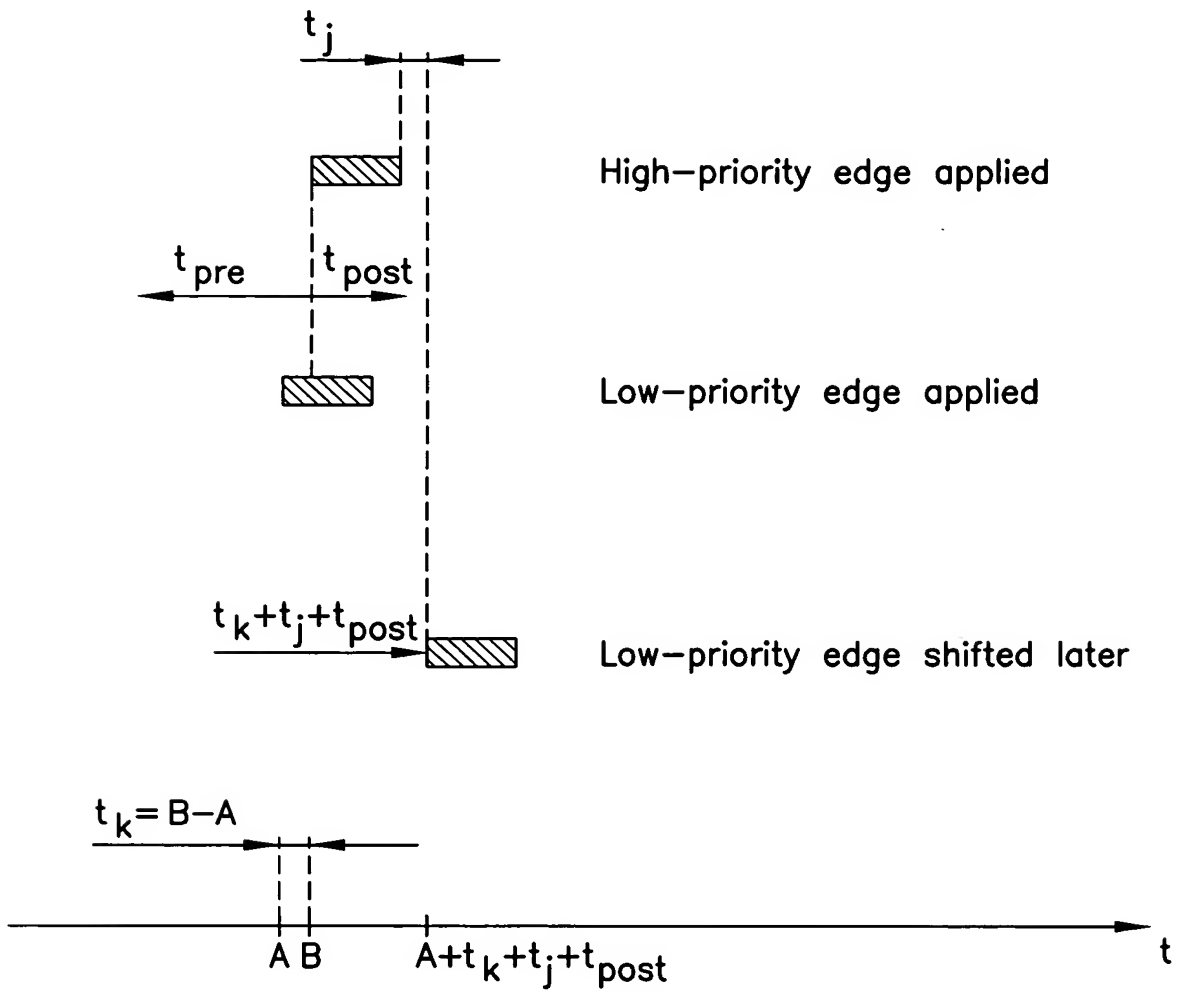


Fig. 6

Shortening of a low-priority triggering action (Calculation)

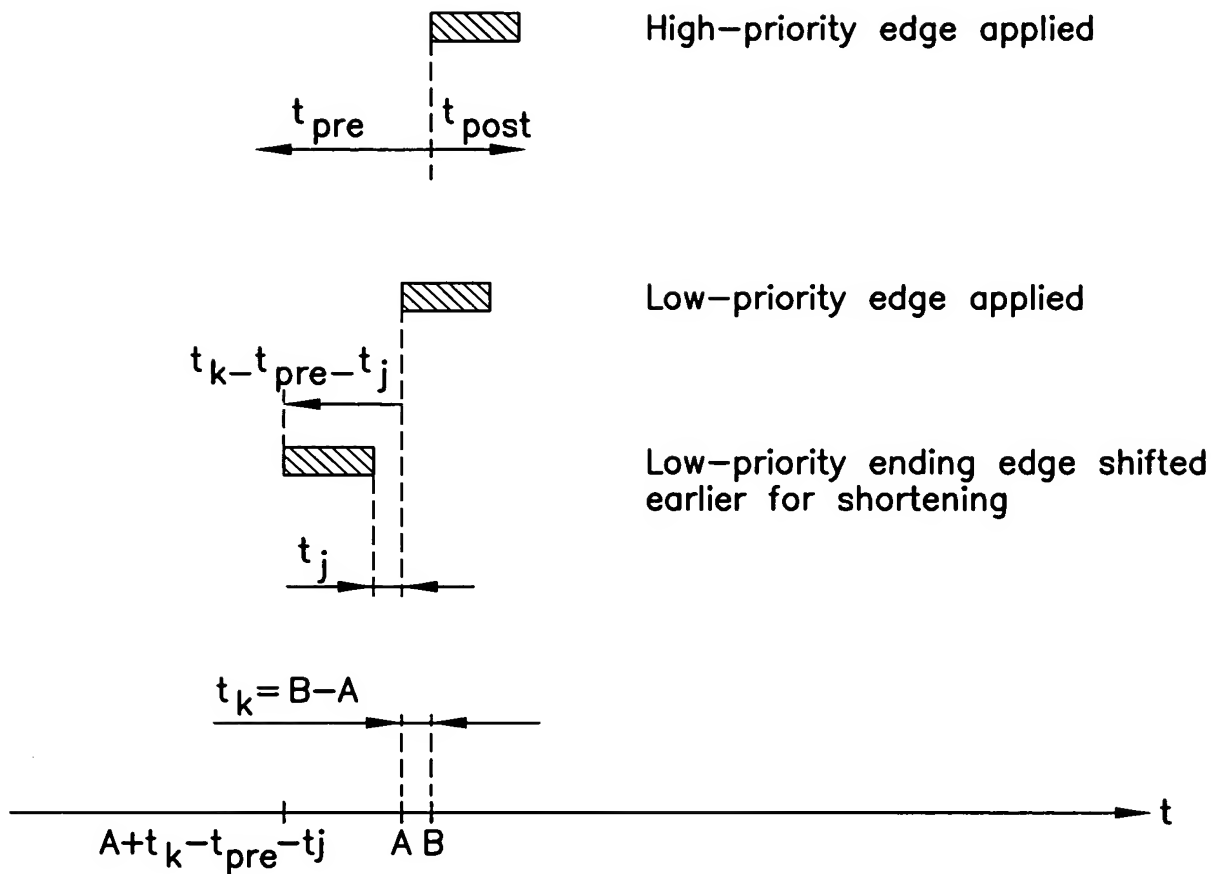


Fig. 7